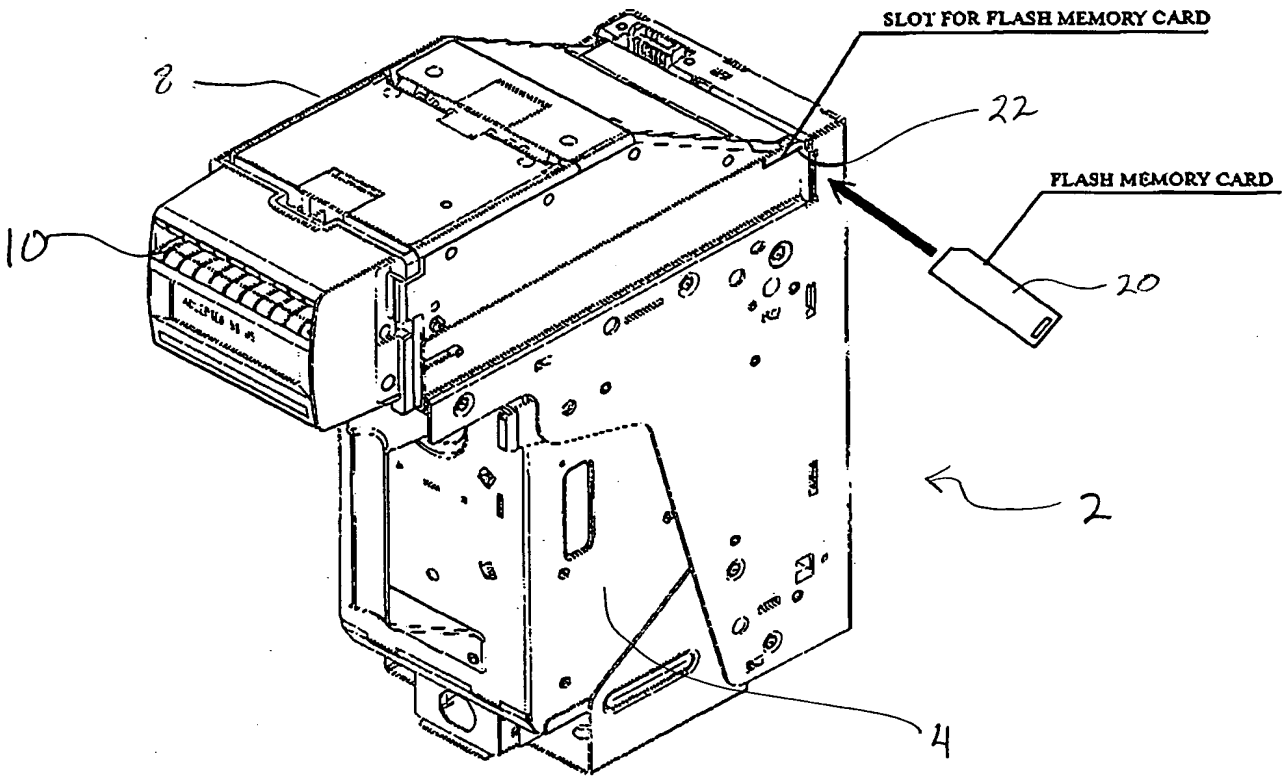

FIG. 1

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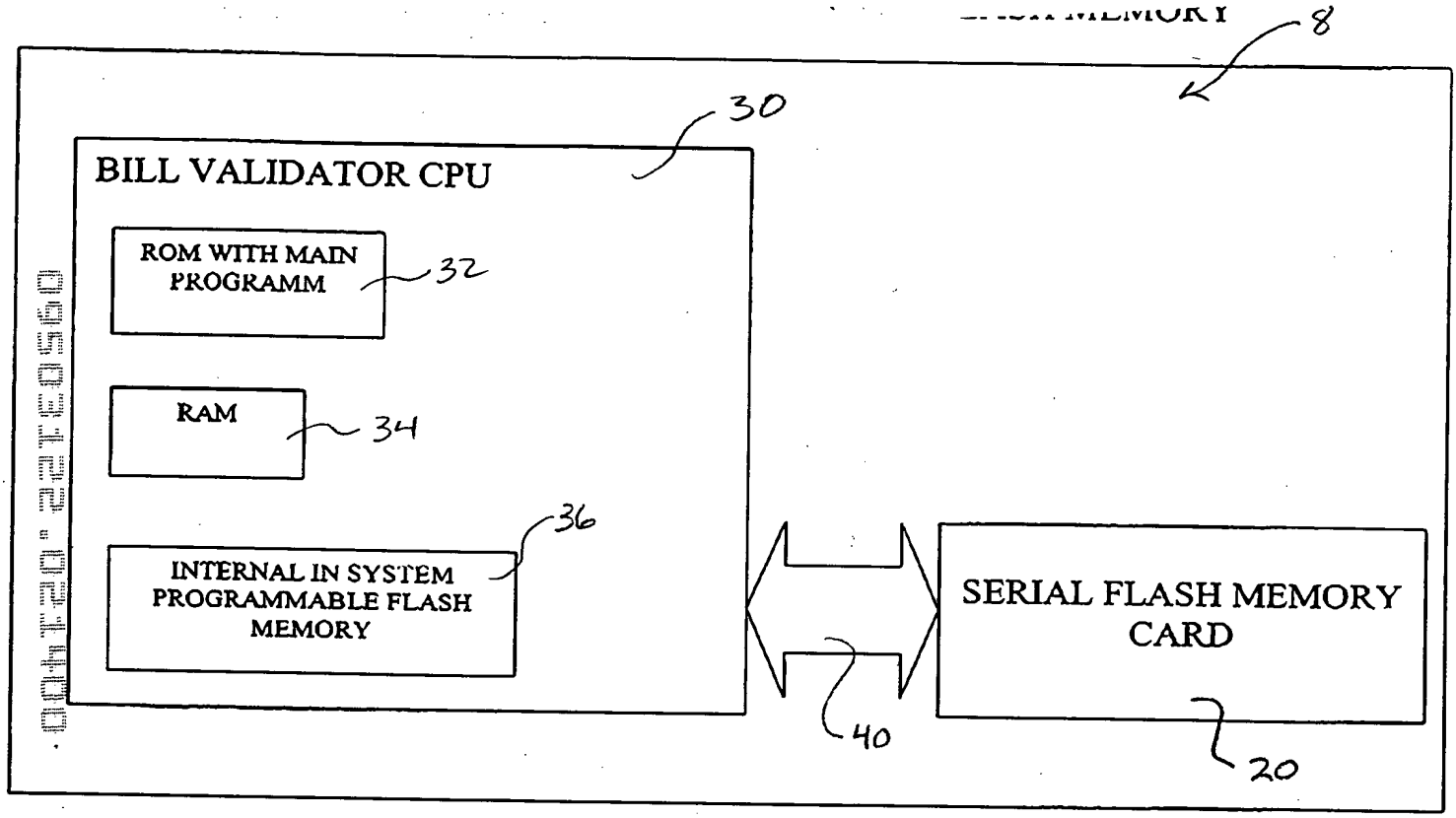
8
30
BILL VALIDATOR CPU
ROM WITH MAIN PROGRAMM
32
RAM
34
36
INTERNAL IN SYSTEM PROGRAMMABLE FLASH MEMORY
40
SERIAL FLASH MEMORY CARD
20

FIG. 2

FLASH CARD MEMORY SPACE

MANUFACTURER ID
PART COMPABILITY
PACKAGE SPEED
TEMP.,VOLTAGE
RESERVED
CHECKSUM MSB
CHECKSUM LSB
RESERVED
SERIAL NUMBER –once programmed memory field
SOFTWARE VERSION
CHANGEABLE PART OF MAIN PROGRAMM FOR DOWNLOAD

FIG. 3

MAIN CONTROLLER MEMORY SPACE

MAIN PROGRAMM
LOADER FOR AUTOMATIC DOWNLOAD
SECURITY SOFTWARE MANAGER
SECURITY DECODER AND INTERNAL FLASH PROGRAMMER

FIG. 4

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FLASH MEMORY CARD INTERFACE WORKING ALGORITHM

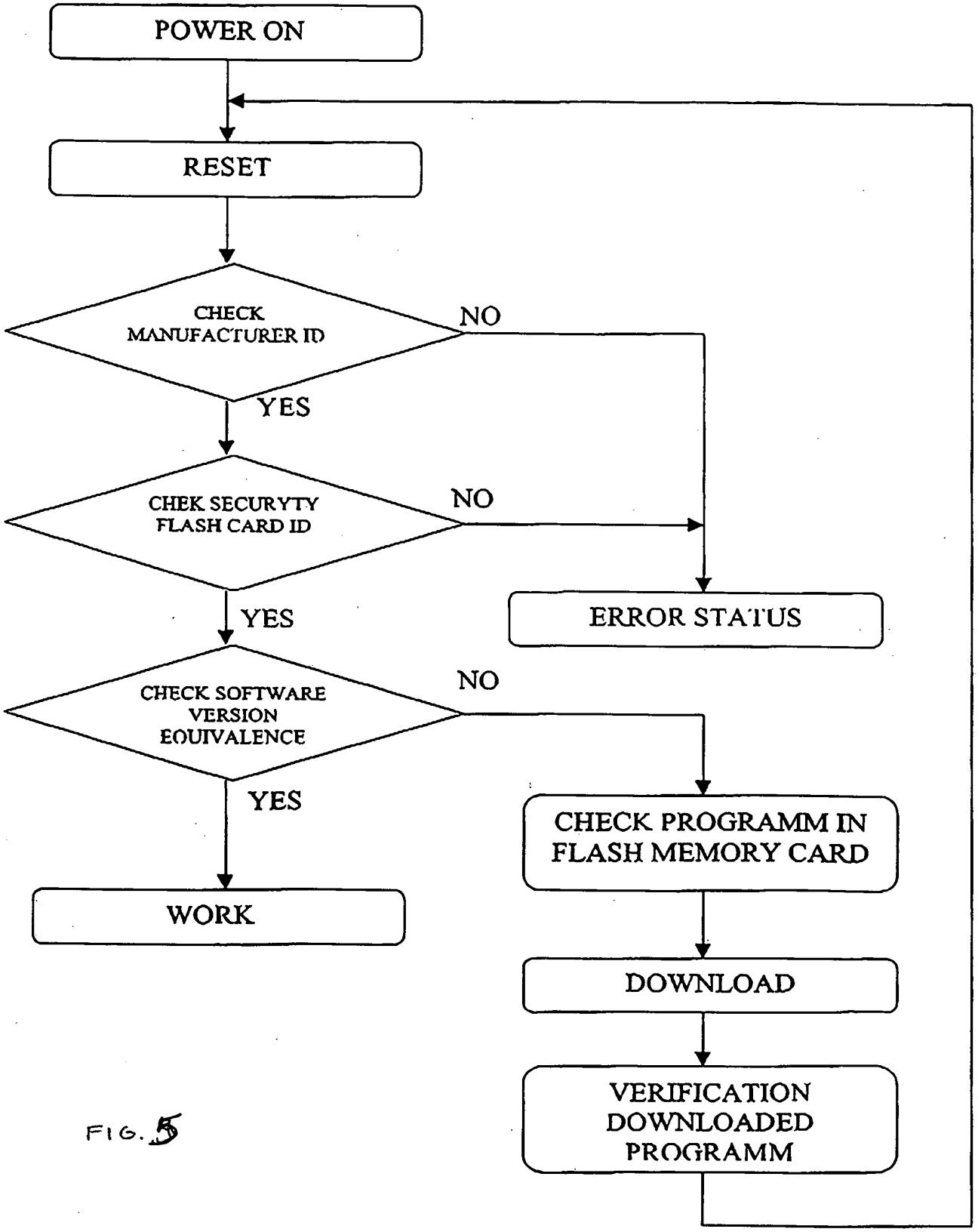

FIG. 5

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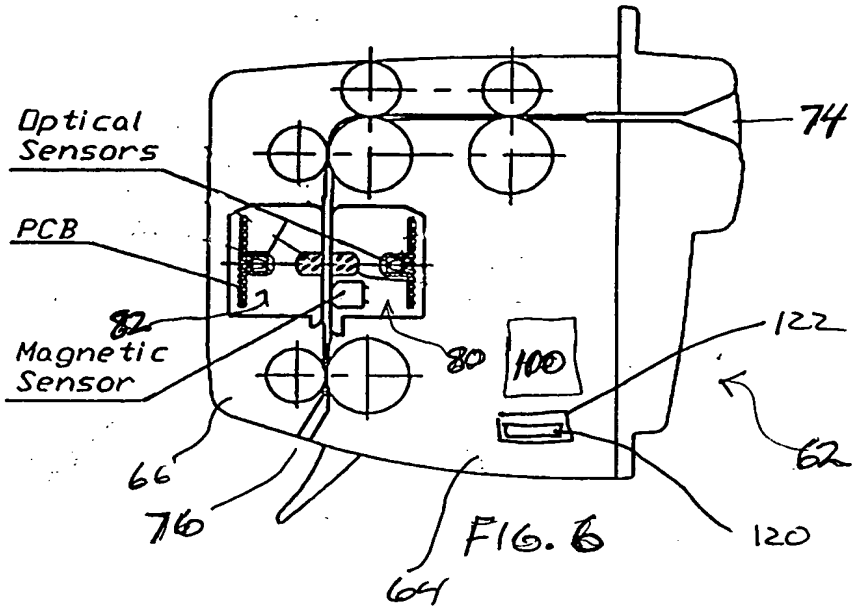

FIG. 6

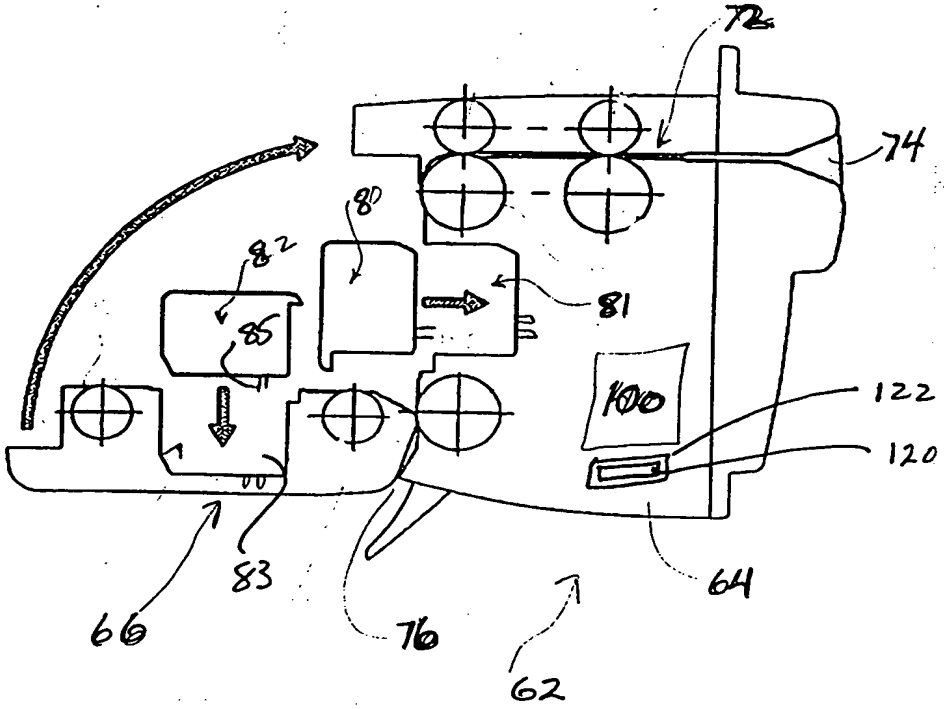

FIGURE 7

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